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**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: Hung T. Nguyen

Serial No.: 10/066,150

Filed: October 26, 2001

Title: **EFFICIENT INSTRUCTION PREFETCH MECHANISM FOR  
DIGITAL SIGNAL PROCESSOR AND METHOD  
OF OPERATION THEREOF**

Grp./A.U.: 2183

Examiner: Meonske, Tonia L.

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF FACSIMILE**  
I hereby certify that this correspondence is being facsimiled as certified to the Patent and  
Trademark Office on March 17, 2005 (Date)  
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Stephen R. Pitt  
Signature

Sir:

**AMENDMENT UNDER 37 C.F.R. § 1.116**

The Applicant has carefully considered this application in connection with the Examiner's  
Final Action mailed December 10, 2004, and respectfully requests reconsideration of this application  
in view of the following amendments and remarks: